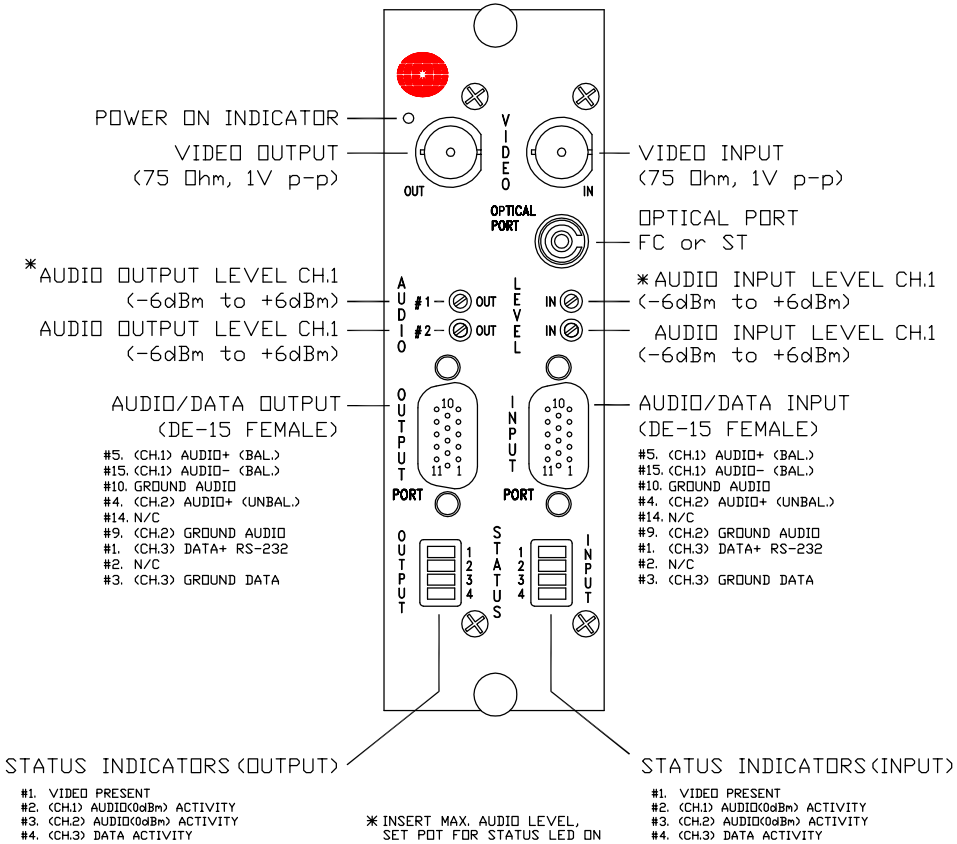
PXA/PXB-800iR-V5A5D3/V5A5D3-X
PINOUT DIAGRAM



#5. (CH.1) AUDIO+ (BAL.)
#15. (CH.1) AUDIO- (BAL.)
#10. GROUND AUDIO
#4. (CH.2) AUDIO+ (UNBAL.)
#14. N/C
#9. (CH.2) GROUND AUDIO
#1. (CH.3) DATA+ RS-232
#2. N/C
#3. (CH.3) GROUND DATA

#5. (CH.1) AUDIO+ (BAL.)
#15. (CH.1) AUDIO- (BAL.)
#10. GROUND AUDIO
#4. (CH.2) AUDIO+ (UNBAL.)
#14. N/C
#9. (CH.2) GROUND AUDIO
#1. (CH.3) DATA+ RS-232
#2. N/C
#3. (CH.3) GROUND DATA

STATUS INDICATORS (OUTPUT)

#1. VIDEO PRESENT
#2. (CH.1) AUDIO(0dBm) ACTIVITY
#3. (CH.2) AUDIO(0dBm) ACTIVITY
#4. (CH.3) DATA ACTIVITY

* INSERT MAX. AUDIO LEVEL, SET POT FOR STATUS LED ON

STATUS INDICATORS (INPUT)

#1. VIDEO PRESENT
#2. (CH.1) AUDIO(0dBm) ACTIVITY
#3. (CH.2) AUDIO(0dBm) ACTIVITY
#4. (CH.3) DATA ACTIVITY

MERIDIAN TECHNOLOGIES INC.
PXAPXB800iR-V5A5D3.DWG
12/19/2006 V.1